IN THE CLAIMS

1. (Previously Presented) A video apparatus with a digital decoder comprising:

a first memory for storing video data;

a second memory for storing on-screen display data;

an on-screen display circuit for generating on-screen display graphics signal from the on-screen display data in the second memory;

wherein the first memory is adapted to receive on-screen display data that is no longer being displayed from the second memory and to transfer said on-screen display data back to the second memory in response to a request for display of data stored in the first memory.

2. (Previously Presented) A video apparatus according to claim 1, further comprising a processing unit, the first memory not being directly accessible by the processing unit.

3. (Cancelled)

4. (Previously Presented) A video apparatus according to claim 1, wherein the first memory is a random access memory used for video decompression.

5. (Original) A video apparatus according to claim 1, wherein the digital decoder is connected to a digital front-end.

6. (Previously Presented) A process for controlling a video apparatus comprising a digital decoder, a first memory, a second memory and an on-screen display circuit for generating an on-screen display signal based on data stored in the second memory, said method comprising the steps of:

writing on-screen display data to the second memory for access by the on-screen display circuit;

wherein, the first memory is used for video decompression, further comprising the steps of:

-transferring on-screen display data that is no longer being be displayed to the first memory; and

-upon request, transferring back on-screen display data from the first memory to the second memory.

7. (Previously Presented) A process according to claim 6, further comprising the steps of:

-        issuing a request for the on-screen display circuit to use more than a given size in the second memory,

-        realizing a direct memory transfer of on-screen display data from the second memory to the first memory.

8. (Previously Presented) A process according to claim 7, further comprising the steps of:

-        issuing a request for the on-screen display circuit to use on-screen display data in the first memory, and

-        transferring said on-screen display data to be used from the first memory to the second memory.

9. (Previously Presented) A process according to claim 7, wherein the transfer of on-screen display data to the first memory occurs when the first memory is unavailable for video decompression.

10. (Previously Presented) A video apparatus according to claim 1, wherein transfer between the first and second memories is made using a direct memory access.

11. (Previously Presented) A video apparatus according to claim 4, wherein the first memory is made available for storing on-screen display where the first memory is not being used for holding video data.